



<u>PROGRAM</u>	NATIONAL DIPLOMA <i>CHEMICAL ENGINEERING</i>
<u>SUBJECT</u>	PROCESS CONTROL
<u>CODE</u>	ICP3111
<u>DATE</u>	SUMMER EXAMINATION 23 NOVEMBER 2019
<u>DURATION</u>	(SESSION 1) 8:30 - 11:30
<u>TOTAL MARKS</u>	100
<u>FULL MARKS</u>	100

<u>EXAMINER</u>	MR G PAHLA
<u>MODERATOR</u>	DR C BHONDAYI
<u>NUMBER OF PAGES</u>	07

REQUIREMENTS : Use of scientific (non-programmable) calculator is permitted
(only one per candidate); graph paper

HINTS AND INSTRUCTIONS TO CANDIDATE(S):

- Purpose of assessment is to determine not only if you can write down an answer, but also to assess whether you understand the concepts, principles and expressions involved. Set out solutions in a logical and concise manner with justification for the steps followed.
- **ATTEMPT ALL QUESTIONS.** Please answer each question to the best of your ability.
- Write your details (module name and code, ID number, student number etc.) on script(s).
- Number each question clearly; questions may be answered in any order.
- Make sure that you read each question carefully before attempting to answer the question.
- Show all steps (and units) in calculations; this is a 'closed book' test.
- Ensure your responses are legible, clear and include relevant units (where appropriate).

Question 1

[Total 20 Marks]

Process control systems

Consider a hot/cold water mixer shown in figure 1.1 below. The design intent is to maintain the temperature of the mixed warm water T_o at some output temperature set point $T_{o,sp}$. The disturbances that can bring instability to the system are;

The flow of cold water F_c and the temperature of the cold water T_c which are determined by upstream processes. The flow of hot water F_H , can be changed by a control valve.

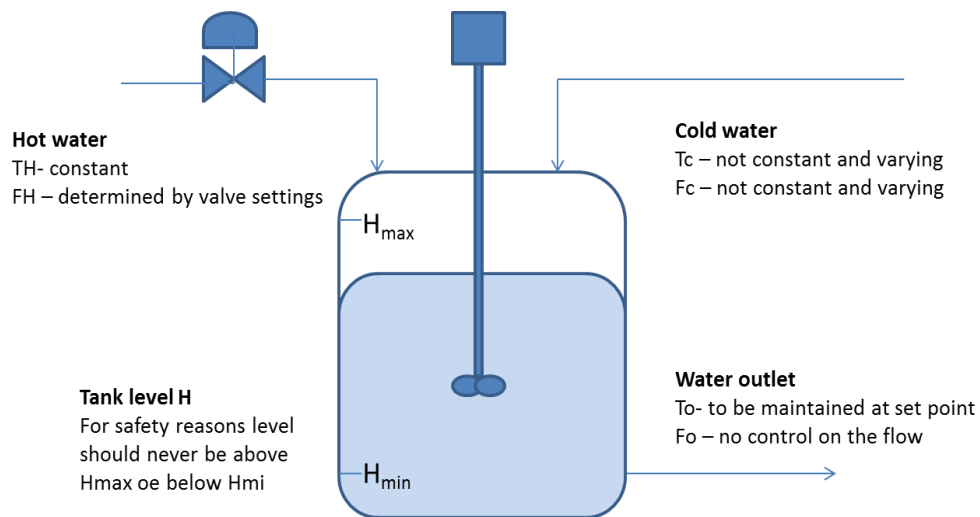


Figure 1.1: Schematic of a tank mixer process flow diagram

1.1. Do not consider level control. Show how you can control the tank mixer system using Feedback control.

Follow the steps below.

- Define the control objective. [2]
- Identify the disturbances. [2]
- Select Measured variables. [2]
- Select manipulated variables. [2]
- Designing a controller, redraw the diagram showing the control configuration.[12]

Question 2

[Total 25 Marks]

2.1. Draw a logic circuit for the following

(a) $F = (A+B).C$ [3]

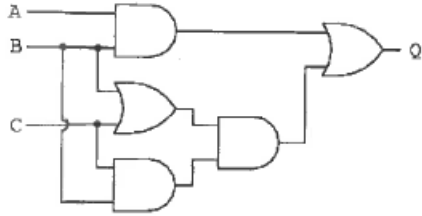
(b) $F = A+B.\overline{C}+D$ [3]

(c) $F = A.B + \overline{A.C}$ [4]

$$(d) F = \overline{(A+B)} \cdot (C+D) \cdot \overline{C}$$

[5]

- 2.2. Find the behaviour expression or alarm setting output for the following circuit (Neatly copy the circuit and label each signal stream and show the final expression Q. [5]



Question 3

[Total 20 Marks]

A control with an equal percentage inherent characteristic has a maximum Cv of 1300 and a rangeability of 40.

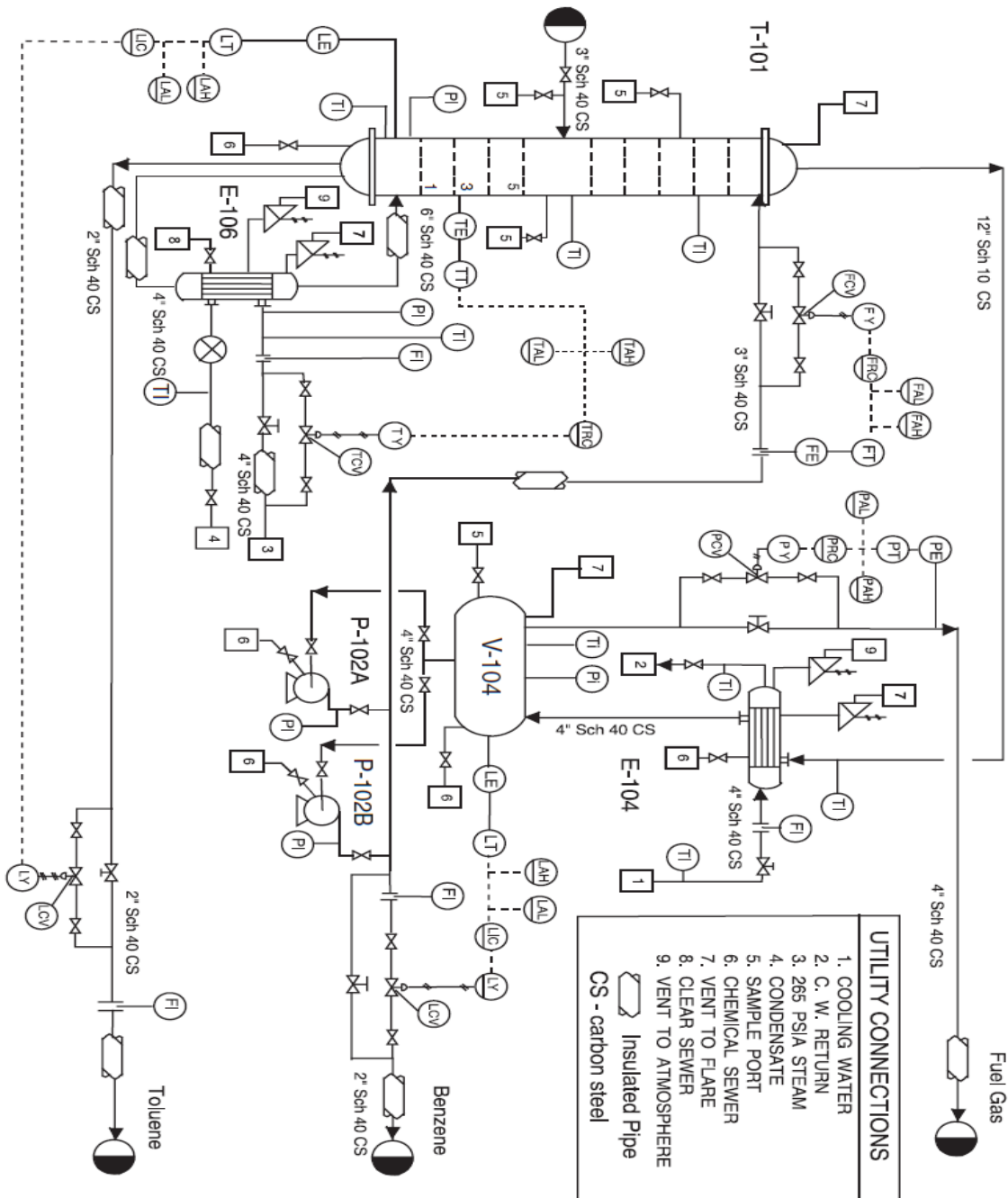
- 3.1. Calculate the % changes in the flow for every 10% change in valve. [5]
 3.2. Calculate the values of Cv corresponding to valve travel from 10% to 40% by step of 10% draw a table containing these values. [10]

Question 4

[Total 20 Marks]

Refer to the P&ID below and answer the following questions:

- 4.1. List all major process equipment by their labels in the P&ID; [10]
 4.2. List and describe all temperature control loops in the P&ID, i.e. state which equipment or line is controlled and what line is manipulated; [3]
 4.3. List and describe all pressure control loops in the P&ID, i.e. state which equipment or line is controlled and what line is manipulated; [3]
 4.4. List and describe all flow control loops in the P&ID, i.e. state which equipment or line is controlled and what line is manipulated. [4]



Question 5

[Total 15 Marks]

5.1. State what process variable each of the below measures:

- Bourdon tube. [2]
- Orifice tube. [2]
- Vortex meter. [2]

5.2. A level measurement system operates with a capacitance based detector. Explain the operating principle and state the advantages and disadvantages of using a capacitance based detector.

Total Marks: 100

Appendix A :Traditional digital logic gate symbols, Boolean functions and truth table

Type	Distinctive shape	Boolean algebra between A & B	Meaning	Truth table																		
<u>AND</u>		$A \cdot B$	Output is true if and only if (<u>iff</u>) both A and B are true	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A AND B</th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	INPUT		OUTPUT	A	B	A AND B	0	0	0	0	1	0	1	0	0	1	1	1
INPUT		OUTPUT																				
A	B	A AND B																				
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<u>OR</u>		$A + B$	True iff A is true, or B is true, or both.	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A OR B</th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	INPUT		OUTPUT	A	B	A OR B	0	0	0	0	1	1	1	0	1	1	1	1
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<u>NOT</u>		$\overline{A}$	True iff A is false.	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th></th><th>NOT A</th></tr><tr><td>0</td><td></td><td>1</td></tr><tr><td>1</td><td></td><td>0</td></tr></table>	INPUT		OUTPUT	A		NOT A	0		1	1		0						
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<u>NAND</u>		$\overline{A \cdot B}$	A and B are not both true.	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A NAND B</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	INPUT		OUTPUT	A	B	A NAND B	0	0	1	0	1	1	1	0	1	1	1	0
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<u>NOR</u>		$\overline{A + B}$	True iff neither A nor B .	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A NOR B</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	INPUT		OUTPUT	A	B	A NOR B	0	0	1	0	1	0	1	0	0	1	1	0
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<u>XOR</u>		$A \oplus B$	True iff A is not equal to B .	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A XOR B</th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	INPUT		OUTPUT	A	B	A XOR B	0	0	0	0	1	1	1	0	1	1	1	0
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