



PROGRAM : NATIONAL DIPLOMA
CHEMICAL ENGINEERING

SUBJECT : **PROCESS CONTROL**

CODE : **ICP 3111**
SUMMER SSA EXAMINATION 2014

DATE : 4 DECEMBER 2014

DURATION : (SESSION 2) 11:30 - 15:30

WEIGHT : 40: 60

TOTAL MARKS : 100

EXAMINER : MRS TP MASHIFANA

MODERATOR : DR H RUTTO

NUMBER OF PAGES : 6 PAGES INCLUDING ANNEXURES

INSTRUCTIONS : QUESTION PAPERS MUST BE HANDED IN.
: CALCULATORS ARE ALLOWED
: NO COMPUTERS ALLOWED
: NUMBER AND ANSWER ALL QUESTIONS IN AN ORDER
: UNIVERSITY EXAM RULES APPLY

Question 1**[40]**

- a) Give the operating principle, advantages and disadvantages for Radar transmitters
- b) Give the difference between a feedback control and a feed-forward control and provide the advantages and disadvantages for each.
- c) Define the following terms:
 - Programmable logic controller (PLC)
 - Ladder logic
- d) Figure 1 shows a stirred batch reactor, a simple feedback control and cascade control can be applied to control the temperature in and around the reactor. Use diagrams to show the difference between simple feedback control and cascade control

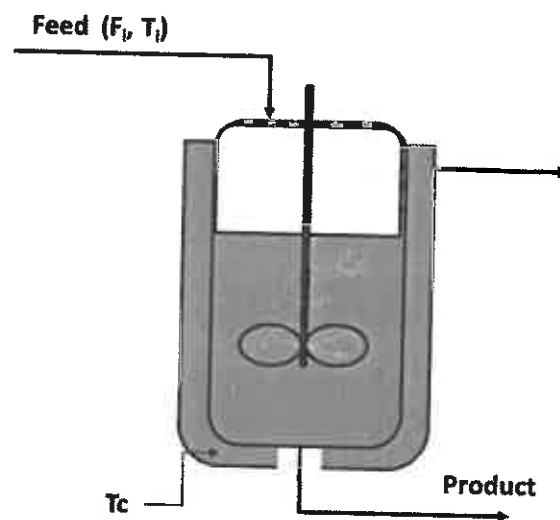


Figure 1: stirred batch reactor

Question 2**[15]**

A pressure transmitter (TT) has a range from 0.5 bar to 1.5 bar which corresponds to a linear analog signal range of 4 mA to 20 mA. Determine the value of pressure that corresponds to a current signal of 15 mA.

N.B: Use SI Units

- (a) Derive the linear formula expressing pressure in terms of current signal.
- (b) Determine the value of the pressure that corresponds to a current signal of 15 mA.





- The temperature of the hot water T_H , which changes as the operation of the geyser is influenced by the use of water around the house.


The temperature of cold water T_C , is constant and cannot be manipulated.


The flow of cold water F_C , can be changed by a control valve to increase or decrease the flow as may be required.


1. Design a feedback control loop for the shower system using and draw a clear picture of the final answer showing process flow diagram with control loop.

Appendix A :Traditional digital logic gate symbols, Boolean functions and truth table

Logic gate symbols, Boolean functions and truth table																						
Type	Distinctive shape	Boolean algebra between A & B	Meaning	Truth table																		
<u>AND</u>		$A \cdot B$	Output is true if and only if (<u>iff</u>) both A and B are true	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A AND B</th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>0</td></tr><tr><td>1</td><td>0</td><td>0</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	INPUT		OUTPUT	A	B	A AND B	0	0	0	0	1	0	1	0	0	1	1	1
INPUT		OUTPUT																				
A	B	A AND B																				
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<u>OR</u>		$A + B$	True iff A is true, or B is true, or both.	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A OR B</th></tr><tr><td>0</td><td>0</td><td>0</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>1</td></tr></table>	INPUT		OUTPUT	A	B	A OR B	0	0	0	0	1	1	1	0	1	1	1	1
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<u>NOT</u>		\overline{A}	True iff A is false.	<table><tr><th>INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>NOT A</th></tr><tr><td>0</td><td>1</td></tr><tr><td>1</td><td>0</td></tr></table>	INPUT	OUTPUT	A	NOT A	0	1	1	0										
INPUT	OUTPUT																					
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<u>NAND</u>		$\overline{A \cdot B}$	A and B are not both true.	<table><tr><th colspan="2">INPUT</th><th>OUTPUT</th></tr><tr><th>A</th><th>B</th><th>A NAND B</th></tr><tr><td>0</td><td>0</td><td>1</td></tr><tr><td>0</td><td>1</td><td>1</td></tr><tr><td>1</td><td>0</td><td>1</td></tr><tr><td>1</td><td>1</td><td>0</td></tr></table>	INPUT		OUTPUT	A	B	A NAND B	0	0	1	0	1	1	1	0	1	1	1	0
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<u>NOR</u>		$\overline{A + B}$	True iff neither A nor B .																			

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<u>XOR</u>		$A \oplus B$	True iff A is not equal to B .																			

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